

Notice of Allowability

Application No.

10/657,457

Examiner

Yuriy Semenenko

Applicant(s)

WALLACE, RAYMOND CURTIS

Art Unit

2841

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 09/11/2006 and Telephone Interview at 09/19/2006.
2. ☒ The allowed claim(s) is/are 1,2,4-9 and 16-30.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: ____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date ____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date ____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date ____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date ____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other ____

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 09/11/2006 has been entered.

Response to Amendment

2. Amendment filed on 09/11/2006 has been entered.
In response to the Office Action dated 05/11/ 2006, Applicants have amended claims 1, 2, 17-19 and 23 - 25.
Claim 10 has been cancelled.
Claims 1-9 and 16-25 are now pending in the application.

EXAMINER'S AMENDMENT

3.1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided


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by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Ms. Kathleen L. Connell (Reg. No 45344) on 09/19/2006.

3.2. The application has been amended as follows:

Claim 2: (Amended) A circuit card assembly, comprising: a printed circuit board (PCB); an electrical component mounted on the PCB, the electrical component having a component edge; and a dielectric ~~[component]~~ block mountable on the PCB at a plurality of angles with respect to the component edge, the dielectric ~~[component comprising]~~ block consisting of a solid dielectric material having a dielectric constant for modifying at least one electrical parameter of the electrical component, wherein the dielectric ~~[component]~~ block mounted at a first angle of the plurality of angles produces a desired modification of the at least one electrical parameter.

Claim 3: canceled. 

Claim 4: (Amended) The circuit card assembly according to claim 2, wherein the dielectric ~~[component]~~ block is attached to the printed circuit board and is disposed on top of the electrical component.

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Claim 5: (Amended) The circuit card assembly according to claim 2, wherein the dielectric [component] block is attached to the printed circuit board and is disposed under the electrical component.

Claim 7: (Amended) The circuit card assembly according to claim 2, wherein the dielectric [component] block is attached to the printed circuit board via adhesive dots attached to the printed circuit board.

Claim 8: (Amended) The circuit card assembly according to claim 2, wherein the dielectric [component] block is attached to the printed circuit board utilizing non-conductive pads attachable from a surface of the dielectric [component] block to the circuit card assembly.

Claim 9: (Amended) The circuit card assembly according to claim 2, wherein the dielectric [component] block is in direct contact with the electrical component.

3.3. Claims 26-30 which correspond to claims 11-15 added.

Please enter the following new claims.

26. A method for modifying an electrical parameter of an electrical component on a surface of a printed circuit board, comprising the steps of:

(a) ~~[mounting the electrical component on a first surface of the circuit board;~~
~~(a)] mounting a~~ placing a dielectric ~~[material to a circuit board and disposing the~~
~~dielectric material]~~ block in proximity to the electrical component, the dielectric block
consisting of a solid dielectric material having a dielectric constant, the dielectric
component modifying ~~[such that]~~ the electrical parameter of the electrical component
~~[changes]~~ as a function of ~~[position or]~~ an orientation of the dielectric ~~[material]~~ block
relative to the electrical component; and

(b) modifying the electrical parameter to a desired electrical parameter by rotating the
dielectric component to a target angle with respect to an edge of the electrical
component.

27. The method according to claim [11] 26, wherein ~~[step(b)]~~ step(a) includes the step
of disposing the dielectric ~~[material]~~ block on top of the electrical component.

28. The method according claim [11] 26, wherein ~~[step(b)]~~ step(a) includes the step of
placing the dielectric ~~[material]~~ block in direct contact with the electrical component.

29. The method according to claim [11] 26, further comprising the steps of:

(c) testing the electrical parameter of the electrical component; and

(d) if the tested electrical parameter is not approximately an intended value, then
rearranging the dielectric ~~[material]~~ block relative to the electrical component until
the tested electrical parameter is approximately the intended value.

30. The method according to claim ~~{11}~~ 26, further comprising the step of:
(c) modifying, in situ, the electrical parameter of the electrical component by changing ~~[the position or]~~ the orientation of the dielectric ~~[material]~~ block relative to the electrical component.

Allowed Claims

4.1. Claims 1-2, 4-9 and 16-30 are allowed.

4.2.1. The following is a statement of reasons for the indication of allowance claims 1 and 20-25:

Limitations “modifying the electrical parameter to a desired electrical parameter by rotating the dielectric block to a target angle with respect to an edge of the electrical component ” in combination with other claimed limitations in independent claims 1 are not disclosed or suggested by the prior art.

4.2.2. The following is a statement of reasons for the indication of allowance claims 2, 4-9, 16-19 and 26-30:

Limitations “block consisting of a solid dielectric material” and “the dielectric block mounted at a first angle of the plurality of angles produces a desired modification of the at least one electrical parameter” in combination with other claimed limitations in independent claims 2 and 26 are not disclosed or suggested by the prior art.


4.3. Rejoining and allowing claims 26-30.

Claims 11-15, withdrawn from consideration pursuant to the restriction requirement, have been rejoined because they contain similar allowed subject matter to the elected claims as explained in the reason for allowance. Since these claims were cancelled, they have been added back as claims 26-30 and amended to include all of the allowable combinations.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yuriy Semenenko whose telephone number is (571) 272-6106. The examiner can normally be reached on 8:30am - 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean A. Reichard can be reached on (571)- 272-2800 ext. 31. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


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